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L Alkalaj, RV Bopanna - Proc. of 3rd Ann. IEEE Symp. on Parallel and Distributed ... - ieeexplore.ieee.org ... processor is a two level hierarchical system of ... the performance of the proposed shared-memory architecture, we ... Memo, Labora- tory of Computer Science, November ... Cited by 6 - Web Search - ieeexplore.ieee.org

#### **Technical Report**

MC Scheduling, P Allocation - informatik.uni-erlangen.de ... Frank Bellosa University Erlangen-Nürnberg Department of Computer Science IV ... 3.3. Hierarchical hash queues without aging ... Memory conscious user-level scheduling ... View as HTML - Web Search - i30www.ira.uka.de - informatik.uni-erlangen.de

#### Towards a distributed memory implementation of Sisal

M Haines, APW Boehm, D Computing - 1991 - ieeexplore.ieee.org ... Matthew Haines\* Wim Böhm Computer Science Department ... vec- tor [4, 16], hierarchical mmory [30 ... distribution, parameter passing, and dynamic memory allocation [13 ... Cited by 4 - Web Search - ieeexplore.ieee.org - Library Search

## Computer-Assisted Generation of PVM/C++ Programs Using CAP.

BA Gennart, JT Gimenez, RD Hersch - PVM, 1996 - diwww.epfl.ch ... Both shared- and distributed-memory architectures are ... attempts to build computer-aided parallelization ... thread : the thread description is hierarchical. ... Cited by 10 - View as HTML - Web Search - diwww.epfl.ch - portal.acm.org - portal.acm.org

The performance implications of locality information usage in shared-memory multiprocessors F Bellosa, M Steckermeier - Journal of Parallel and Distributed Computing, 1996 - iits.uap.edu.ph

... Department of Computer Science IV, University of Erlangen-Nürnberg ... as a consequence of non local memory accesses (see Fig. ... distributed hierarchical structures ... Cited by 19 - View as HTML - Web Search - cs.fau.de - informatik.uni-erlangen.de - portal.acm.org - all 9 versions

## Management of heterogeneous parallelism on shared memory multiprocessors

AB Tayyab, JG Kuhl - 1990 - ieeexplore ieee org ... Center for **Computer** Aided Design University of Iowa ... **Memory** allocation is a costly operation as it ... The simplest **dynamic** loop scheduling algorithm is called self ... Web Search - ieeexplore ieee org - Library Search

#### Distributed management by delegation

GIS Goldszmidt, Y Yemini - 1996 - doi.ieeecs.org

... Goldszmidt and Yechiam Yemini Computer Science Department ... efficient asynchronous communications between DPISOVer shared memory. ... agent and a hierarchical manager ... Cited by 195 - Web Search - doi.ieeecomputersociety.org - simpleweb.org - wwwsnmp.cs.utwente.nl - all 9 versions »

#### Multi-threaded design for a software distributed shared memory system

JC Ueng, CK Shieh, SC Mac, AC Lai, TY Liang - IEICE Transactions on Information and Systems, 1999 - ee.psu.ac.th

... Thread management in Cohesion can be divided into three main ... a hierarchical mechanism for barriers, called a hierarchical barrier ... 3.4 Dynamic Memory Allocation ...

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[PS] In the Proceedings of the 11th Sympo sium on Computer Architecture and High Performance Computing ( ....

P Kakulavarapu - cos.ufrj.br

... of Electrical and Computer Engineering Newark, USA amaral ... the ap- plication, called the **Hierarchical** Task Graph. ... in a distributed **memory** environment, especially ... View as HTML - Web Search

Overview of the JUMP-i, an MPP Prototype for General-Purpose Parallel Computations

T Kudoh, H Nakashima, H Nakajo - ieeexplore ieee.org

... directory nsetIsod that combines a hierarchical fulimap method. ... implemented by fiae-grained Memory-Based Processor. ... on Computer A,chitecture, pp.290-289 1980. ... Web Search

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ART Perspective - ieeexplore.ieee.org

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## A Bibliography of Supercomputing'93

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... Compute [Mil93]. Computer [Was93]. ... Driven [GGL93, PY93]. Dynamic [CMMZ93, DFWM93]. ... Memory [BKP93, BCF + 93, cC93, HS93, Lv93, LS93b, ZI93, iMSG + 93]. ... View as HTML - Web Search - math.utah.edu

#### IPSI Low-complexity Video Coding for

RL Multicast - cs.umn.edu

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USEP Agency, P Philadelphia - epa.gov

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Visiblity processing, Scenegraphs - by GRAPHICS - 0 citations A survey of design techniques for system-level dynamic ... - by Benini - 143 citations Microsoft Repository Version 2 and the Open Information ... - by Bernstein - 53 citations

#### [PDF] COMPUTER STANDARDS & INTERFACES

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Microsoft internet information server API. ISDN. Integrated Services Digital Network

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#### IPDFI Acronyms / Forkortelser AB

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... Structure Description DSI Dynamic Skeleton Interface ... DSM Distributed Shared Memory

DSM Distributed ... Speed Serial Interface HSM Hierarchical Storage Management ...

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#### (PDF) MICROSOFT REPOSITORY VERSION 2 AND THE OPEN INFORMATION MODEL

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research.microsoft.com/~philbe/ Info%20Sys%20on%20MS%20Repository%20for%20Web.pdf - Similar pages

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... CD-XA: Compact Disc Read Only Memory Extended Architecture ... CDTV: Commodore Dynamic

Total Vision, a consumer ... and mode HFS: Macintosh's Hierarchical Filing System ...

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locality is achieved, resulting in very few main memory. accesses. On the other

hand, ... This appendix gives the examples for IML API functions. ...

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is presented in [9], resulting in a hierarchical mesh reduction technique with
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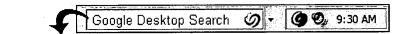
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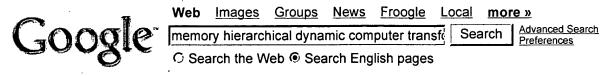
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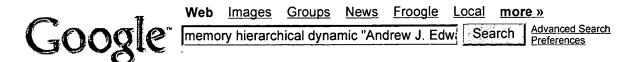
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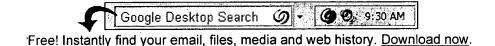


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<u>Translation and transformation of heterogeneous programs patent</u> inventors: Ronnie I. Chaiken, Hon Keat W. Chan, **Andrew J. Edwards**, ... Reed Hastings and Bob Joyce, Purify: Fast Detection of **memory** leaks and access Errors ... www.freshpatents.com/ Translation-and-transformation-of-heterogeneous-programs-dt20050113ptan20050010891.php - Supplemental Result - Similar pages



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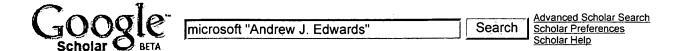
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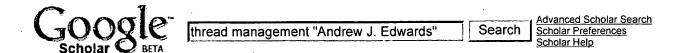
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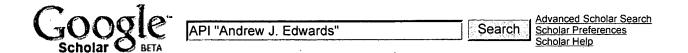


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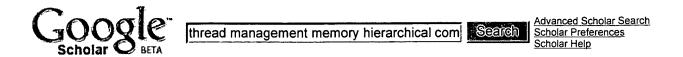
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